



— Prof. Jaehyoun Choi —

Education

- 1999 B.S. in EE, SNU
- 2008 M.S in ECE, Georgia institute of Technology
- 2010 Ph.D in ECE, Georgia institute of Technology

Experience

- Senior engineer, Qualcomm (2010-11)
- Assistant Prof., UNIST (2011-2016)
- Associate Prof., UNIST (2016-2019)
- Associate Prof., KAIST (2019-2023)
- Associate Prof., SNU (2023-Present)

연구실 현황

Graduate students

- 8 Ph.D candidates
- 7 M.S. candidates

Alumni

- 8 Ph.D graduates
- 1 M.S. graduates

Publication

- (Int. conference & Int. journal) +90
- Recent 7 years, ISSCC 20편, JSSC 20편

Award

- IEEE-IEIE Young Engineer of the Year, 2022
- 대한민국반도체설계대전, 대통령상, 2021
- 삼성휴먼테크 논문대상 9회 (은상 5, 동상 3, 장려상 1)

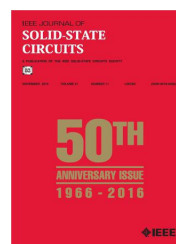
Contact

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20 ISSCC Papers



20 JSSC Papers



9 Samsung Human-Tech



Communication system w/ Two Pillars having complementary advantages

Wired Interconnects/Interfaces

+ Wireless Communications

Reliable, Secure
& High speed

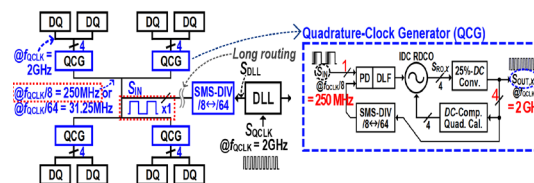
Mobile, Massive connections
& Long distance

**To Build up the Two Pillar Technologies for Future IT Infrastructure
on which any of Future Ideas can be realized**

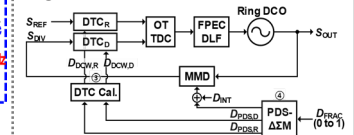


Future High-speed Wired Interconnection/Interface

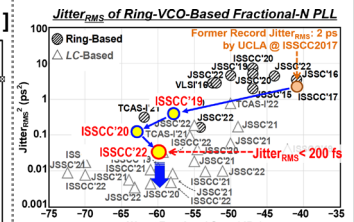
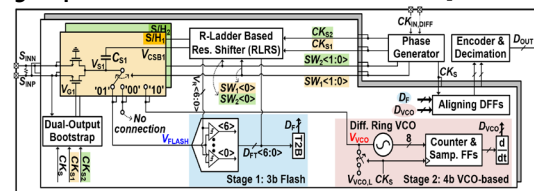
Clock Distribution for DRAM Interface [ISSCC'23]



Area-efficient, Low-jitter
Ring-oscillator-based Frac.-N
Digital PLL [ISSCC'22]

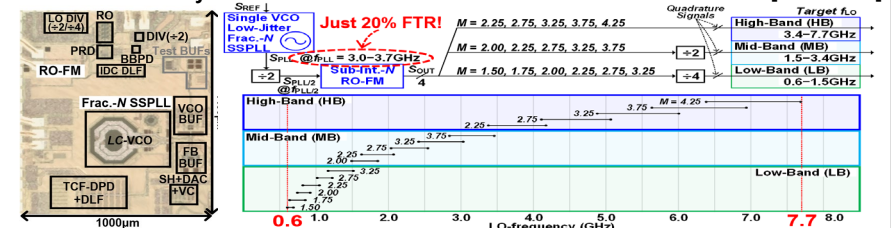


High-speed ADC for DSP-based PAM4 TRX [CICC'23]



Advanced Wireless Communication

Multi-Band & Low-jitter Cascaded Local-Oscillation Generator for 5G [ISSCC'23]



Sub-THz (>100GHz) PLL for 6G [ISSCC'23]

